

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Alexander L. Barr et al GROUP ART UNIT: 2823
APPLN. NO.: 10/780,143 EXAMINER: William D. Coleman
FILED: February 17, 2004
TITLE: SEMICONDUCTOR STRUCTURE HAVING STRAINED
SEMICONDUCTOR AND METHOD THEREFOR

Certificate of Submission

I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.

Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
 Facsimile transmitted in accordance with C.F.R. § 1.6(d).
 Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

1-23-07

Date of Submission

/Stacie Herrera/
Signature

Stacie Herrera

Printed Name of Person Signing Certificate

SUBMISSION OF SUPPLEMENTAL DECLARATION AND
COMBINED POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Supplemental Declaration and Combined Power of Attorney is being filed to correct a minor defect.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079.

Respectfully submitted,

SEND CORRESPONDENCE TO:

Freescale Semiconductor, Inc.
Law Department

Customer Number: 23125

By: *James L. Clingan, Jr.*
James L. Clingan, Jr.
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